

VIRTUAL MEMORY TRANSLATION CONTROL

BY TLB PURGE MONITORING

Tony M. Brewer

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FIG. 1

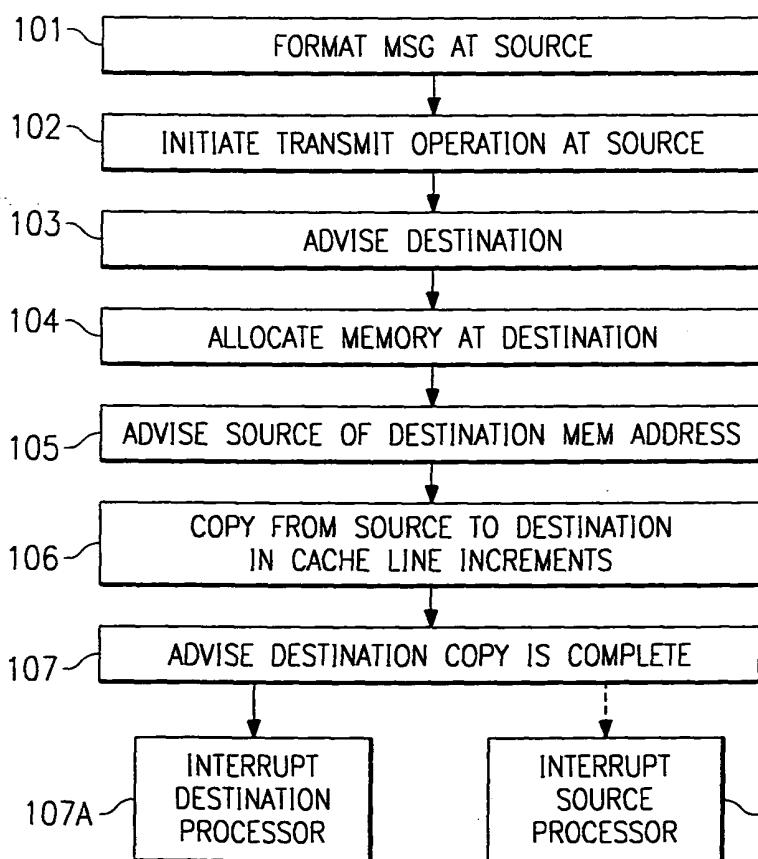
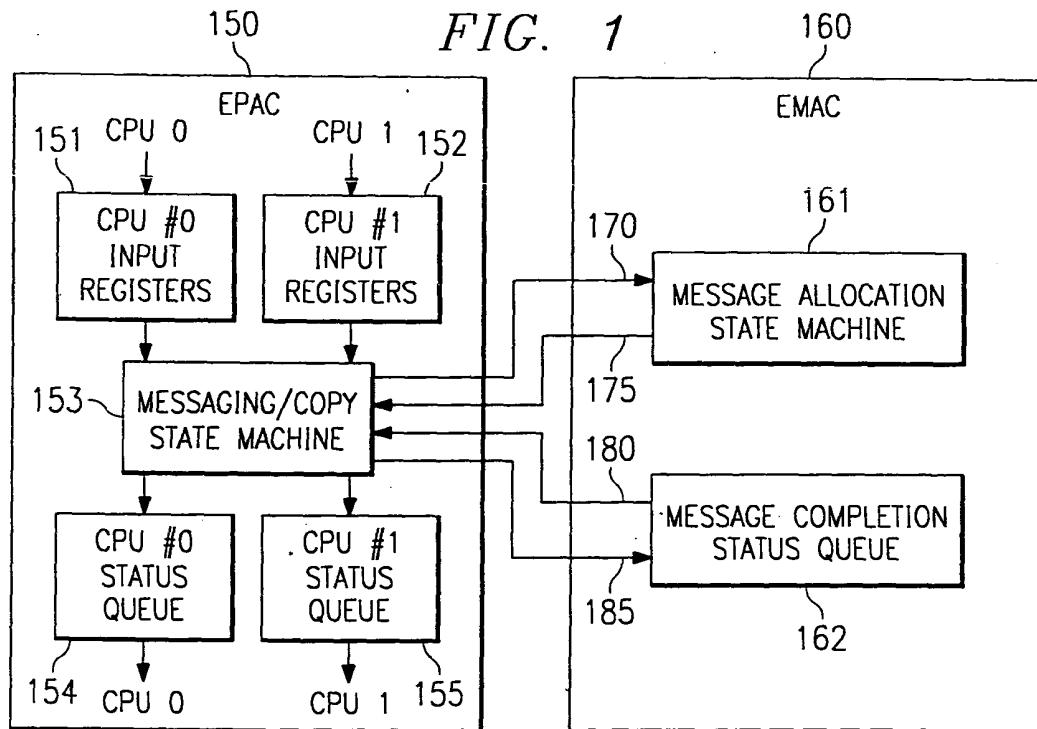


FIG. 1A

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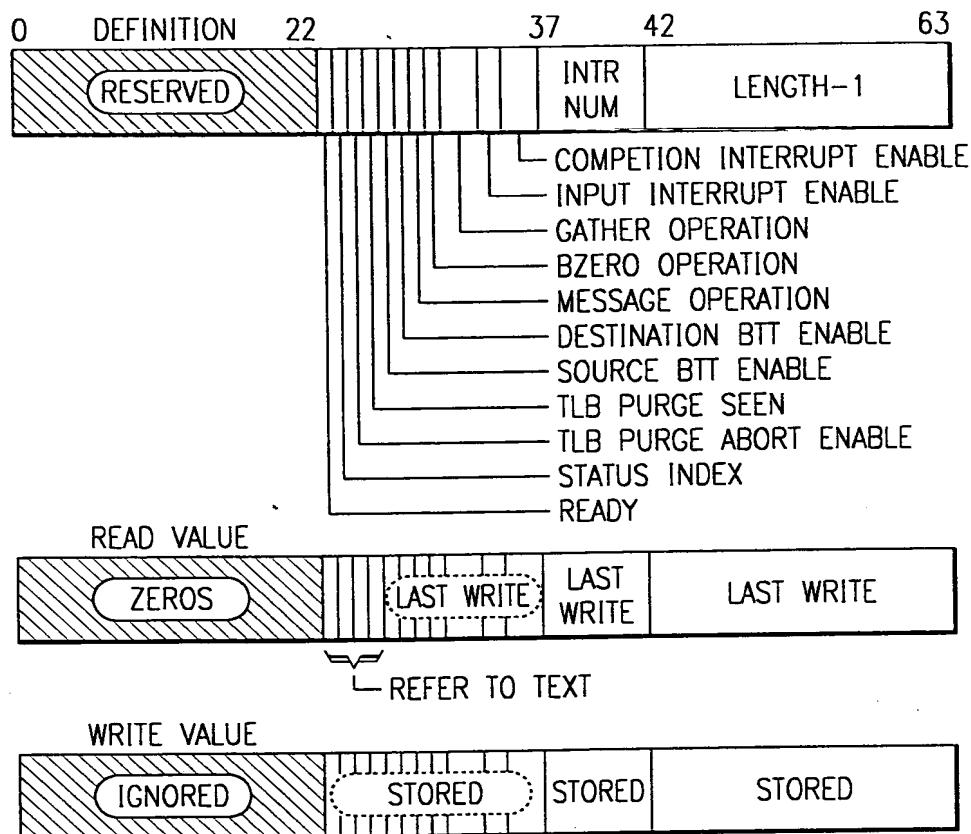


FIG. 2

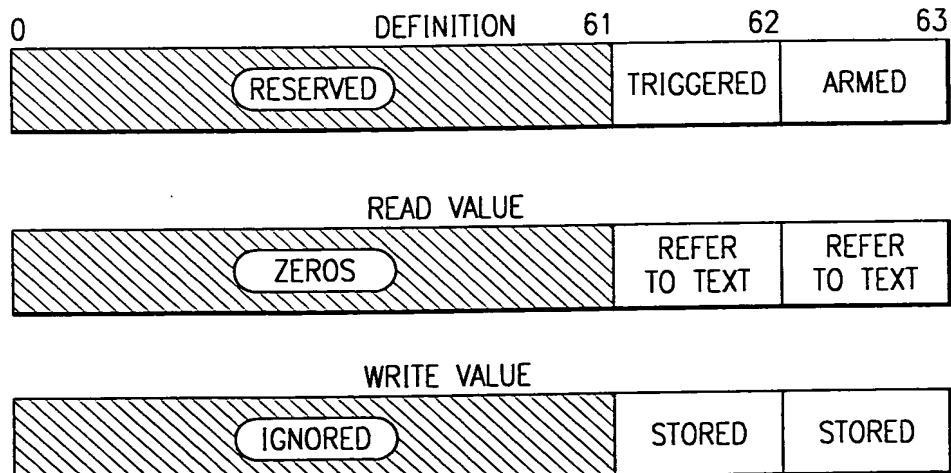


FIG. 2A

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FIG. 3

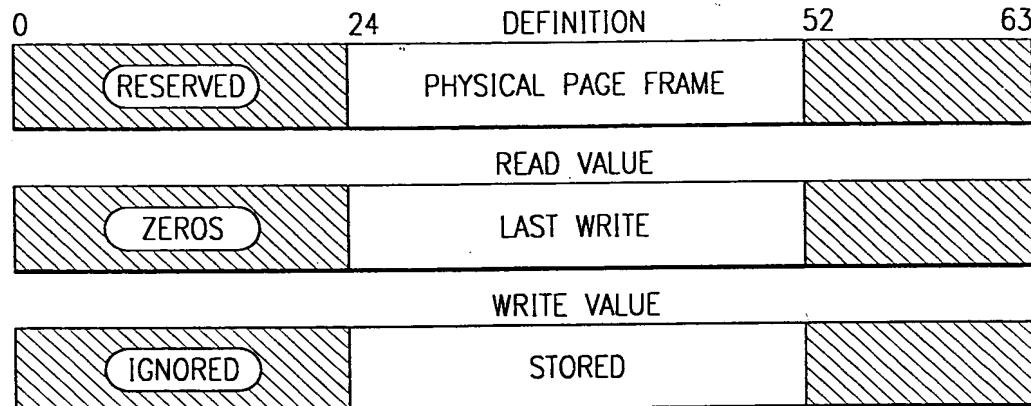


FIG. 4

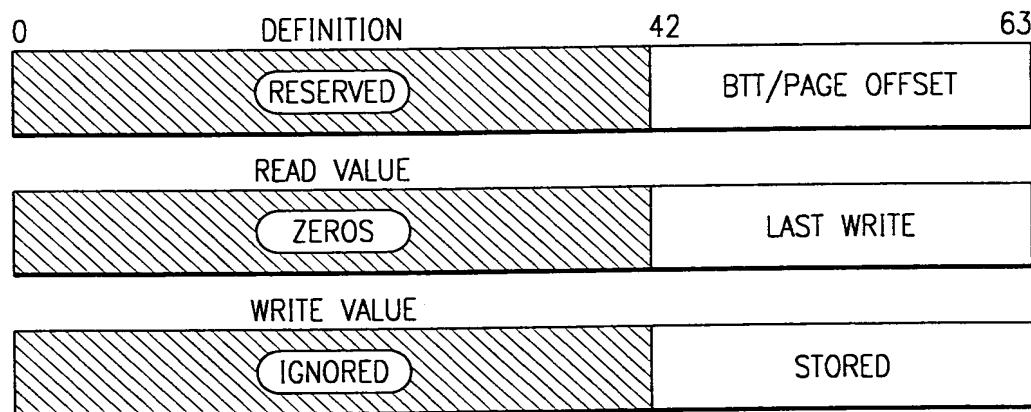
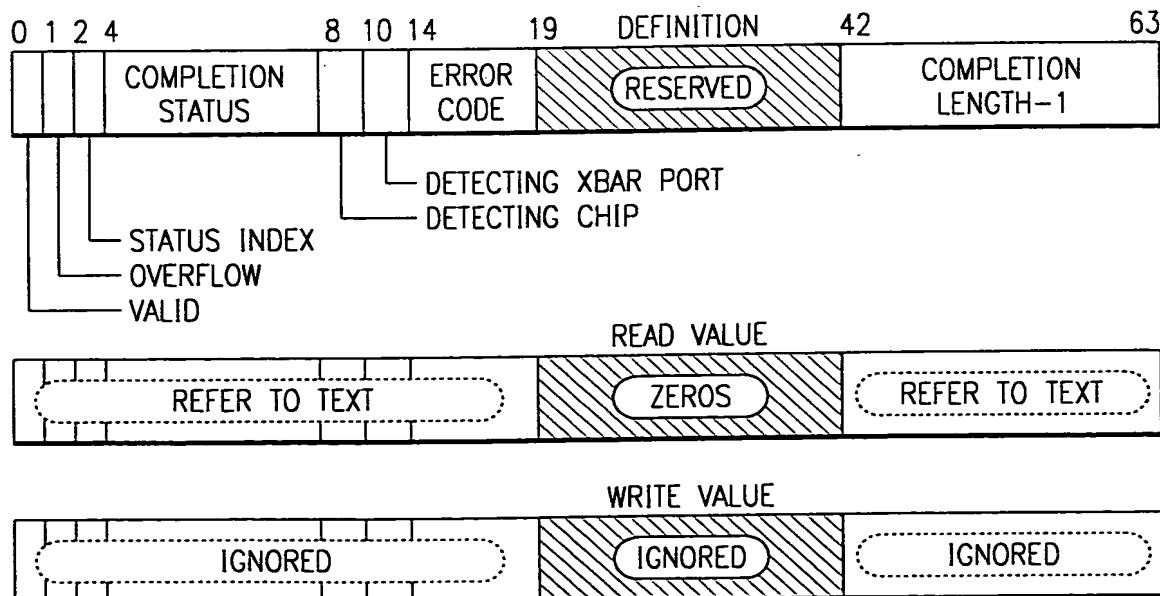


FIG. 5



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FIG. 6

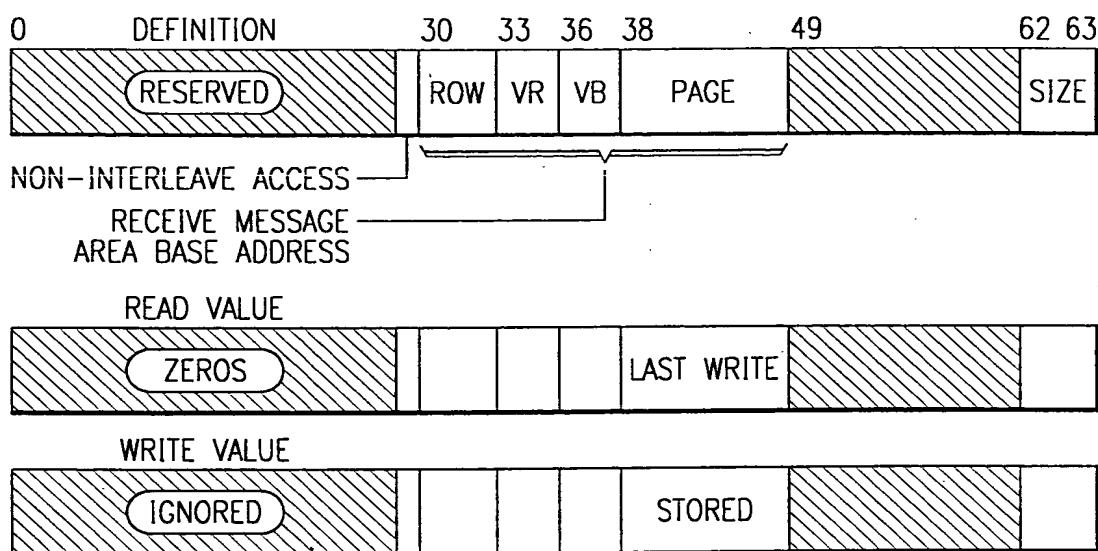


FIG. 7

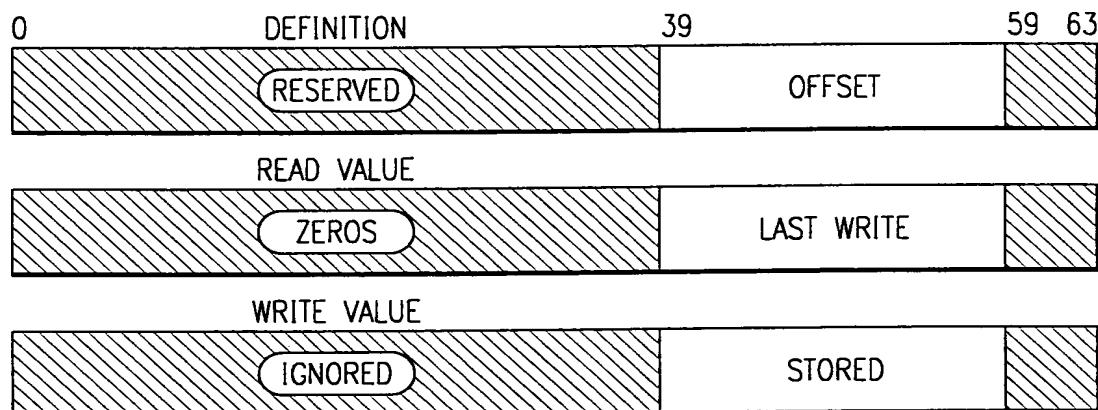
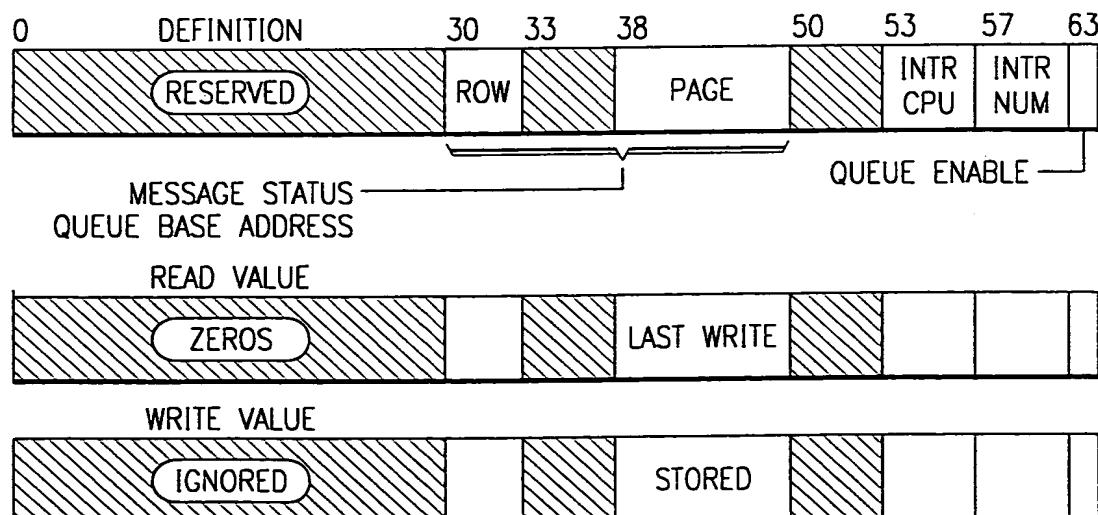


FIG. 8



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FIG. 9

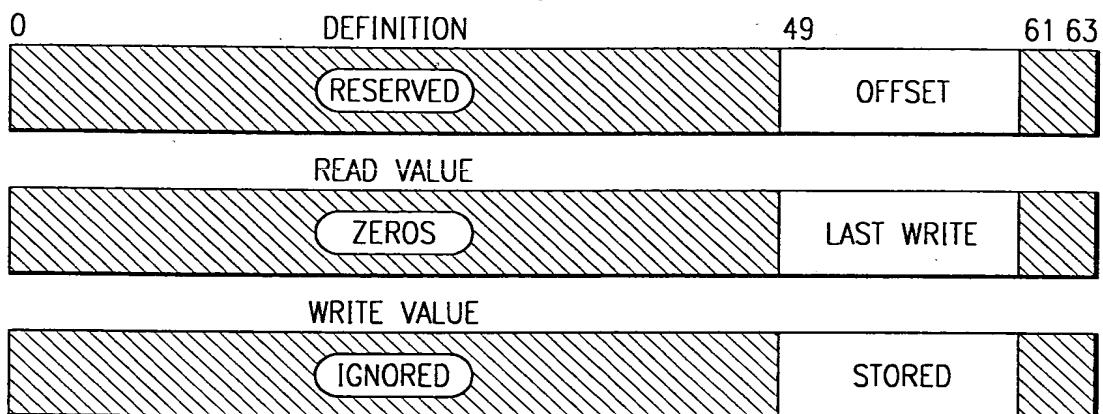


FIG. 10

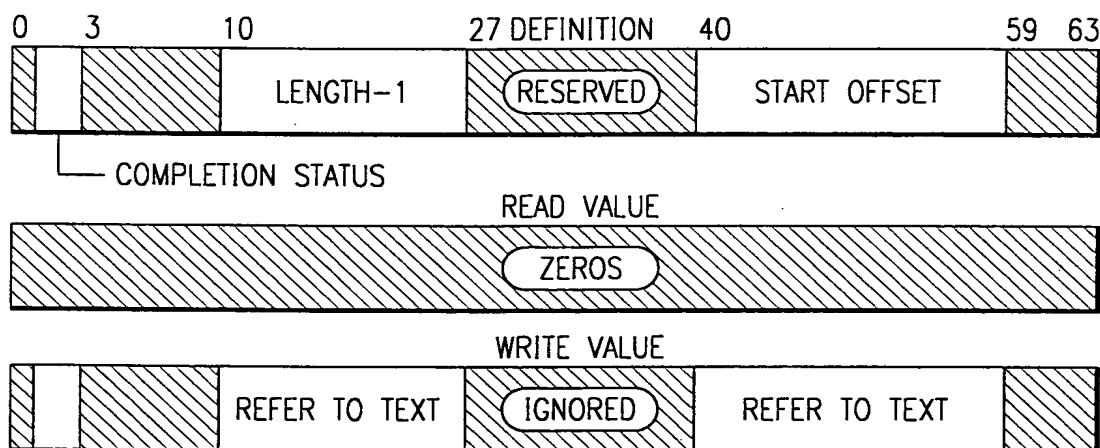
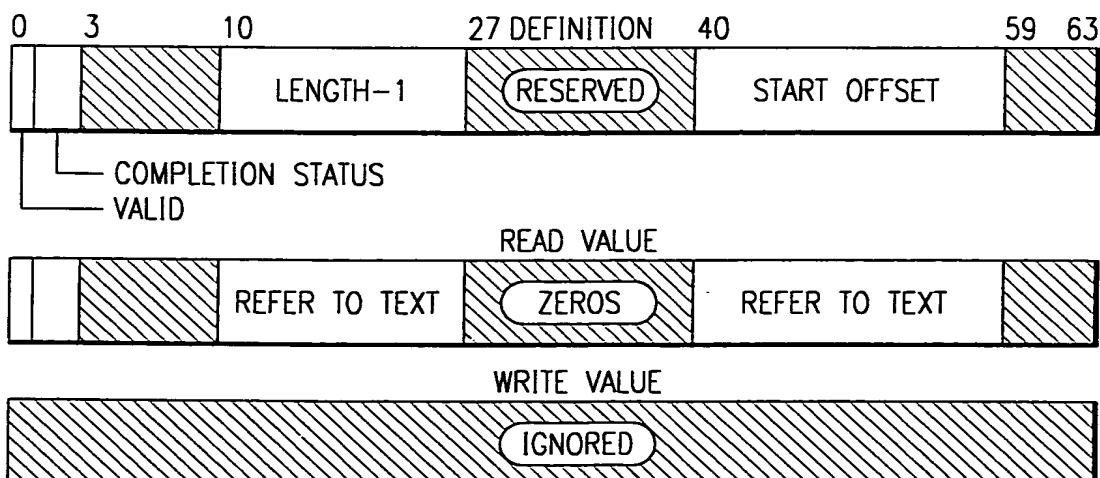


FIG. 11



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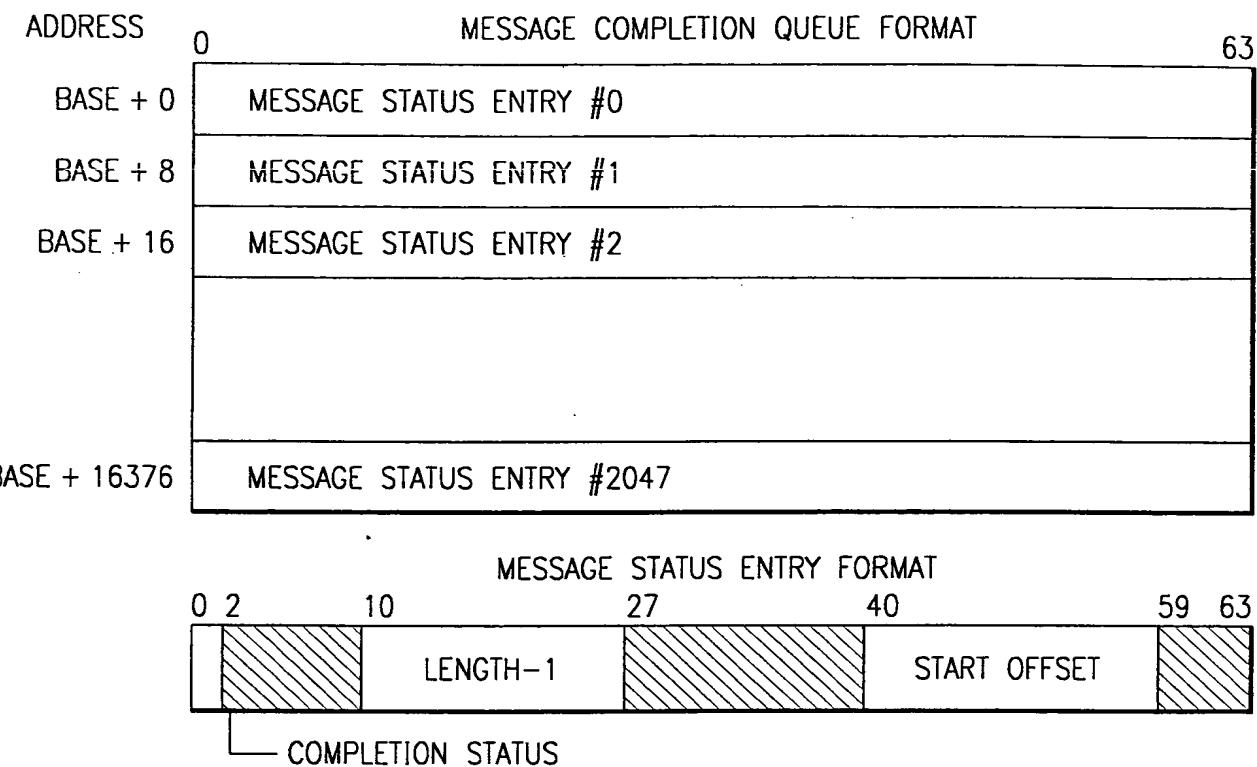
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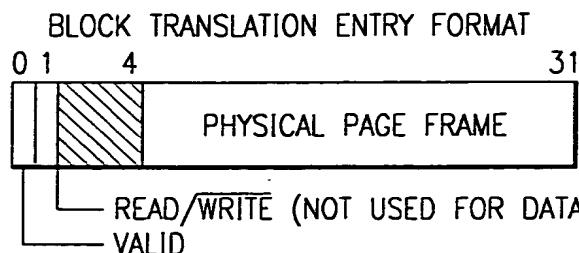
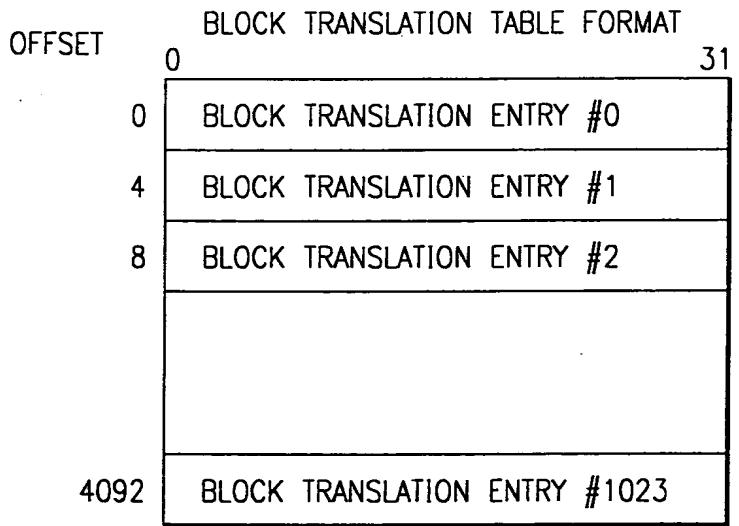
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*FIG. 12*



*FIG. 13*



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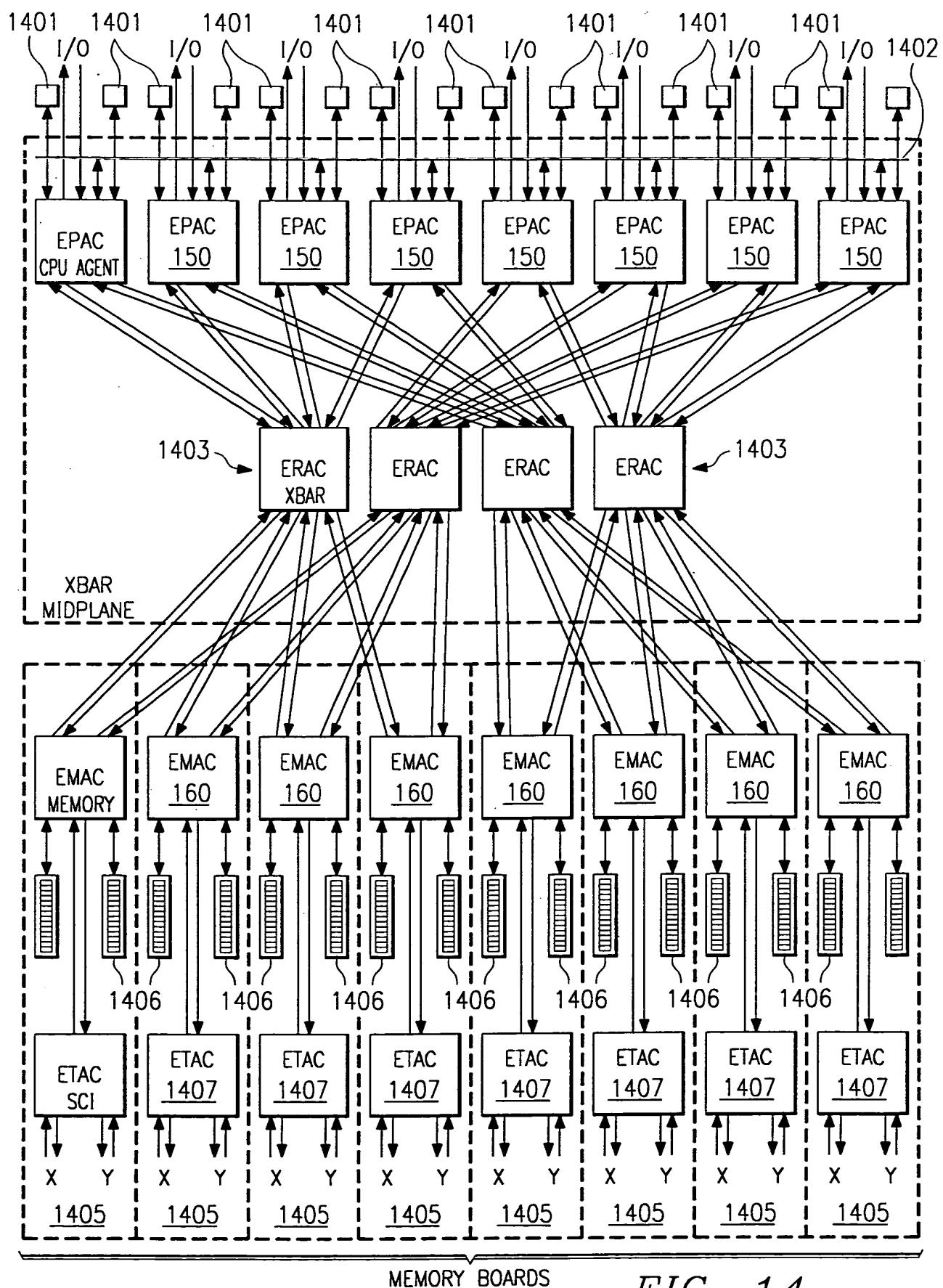


FIG. 14

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